

10/611910

Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO. 501.36384CC9	SERIAL NO. TBD
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANT FUJISAWA, et al.	
		FILING DATE July 3, 2003	GROUP 2814

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
PC	AA 6,020,218	2-1-00	Shim, et al.			
PC	AB 5,874,784	2-99	Aoki, et al.			
PC	AC 5,756,380	5-98	Berg, et al.			
PC	AD 5,917,234	6-99	Tsuruzono			
PC	AE 5,218,234	6-93	Thompson, et al.			
PC	AF 5,590,462	1-97	Hundt, et al.			
PC	AG 5,886,409	3-99	Ishino, et al.			
PC	AH 5,592,025	1-97	Clark, et al.			
	AI					
	AJ					
	AK					
	AL					

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation /Abstract	
						Yes	No
	AM 0702404	9-95	Europe				
	AN 0694965	1-96	France				
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

PC	AU	"Various Types of BGA Packages that Seal 100-MHz LSIs", <u>Nikkei Electronics</u> , Feb. 28, 1994.
	AV	
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Examiner PHAT X. CAO		Date Considered 3/31/04